

GT4899B Low Power, Dual SIM Card Analog Switch

1 Features	2 Application
<ul style="list-style-type: none"> - Low on-resistance: 3.5 Ω @ 4.5 V(typ) - Low on-resistance flatness: 1 Ω @ 4.5 V(typ) - Operate from 1.65 V to 5.5 V - Rail-to-Rail signal switching range - Fast switching speed:14ns @ 3.3 V(typ) - High off isolation:-73dB @ 1MHz - Crosstalk:-89dB @ 10MHz - -3dB bandwidth:350MHz - Package:QFN-3x3-16L and UTQFN1.8x2.6-16L 	<ul style="list-style-type: none"> - Dual SIM card switch - Cell phones - Pad - Digital cameras - PDAs - Notebook

3 Description	Circuit Diagram
<p>The GT4899 is a QPDT switch with one common control inputs targeted at dual SIM card multiplexing. It is optimized for switching the WLAN-SIM data and control signals and dedicates one channel as a supply-source switch.</p> <p>The switches are fully bi-directional, allowing both multiplexing and de-multiplexing operation. Break-before-make operation is guaranteed.</p> <p>The device operates from a +1.65V to +5.5V supply and over the extended -40°C to +85°C temperature range. It is available in QFN-3x3-16L and UTQFN1.8x2.6-16L.</p>	<p>The circuit diagram shows a central vertical dashed line representing the switch mechanism. On the left, there are five control inputs: V_{SIM}, RST, CLK, DAT, and SEL. On the right, there are eight outputs: 1V_{SIM}, 2V_{SIM}, 1RST, 2RST, 1CLK, 2CLK, 1DAT, and 2DAT. The SEL input is connected to an inverter, which then controls the switch mechanism. The V_{SIM} input is connected to the top switch mechanism, which can route the signal to either 1V_{SIM} or 2V_{SIM}. The RST input is connected to the middle switch mechanism, which can route the signal to either 1RST or 2RST. The CLK input is connected to the bottom switch mechanism, which can route the signal to either 1CLK or 2CLK. The DAT input is connected to the bottom switch mechanism, which can route the signal to either 1DAT or 2DAT.</p>

带格式的: 边框: 底端: (无框线)

带格式的: 段落间距段前: 0.5 行

带格式的: 边框: 底端: (无框线)

4 Revision History

Revision	Date	Note
Rev. A10.04	2025. 01. 210	Original Version

The latest datasheet version should be checked on the GTIC official website, as the company does not actively inform customers about updates to the datasheet.



5 Device Summary, Pin and Packages

Table 5-1. Device Summary⁽¹⁾

Serial Name	Part Name	Package	Body Size (Nom)	Marking ⁽²⁾⁽⁴⁾	MSL ⁽³⁾	Package Qty
GT4899B	GT4899BQE	QFN3x3-16L	3.00mm × 3.00mm × 0.75mm	GT4899B XXXXX	3	Tape and Reel,5000
GT4899B	GT4899BUE	UTQFN1.8x2.6-16L	1.80mm × 2.60mm × 0.55mm	4899B XXXX	3	Tape and Reel,3000

(1) For all available packages, please contact product sales.

(2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.

(3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.

(4) "XXXXX" in Marking will be appeared as the batch code.

5 Device Summary, Pin and Packages(Continued)

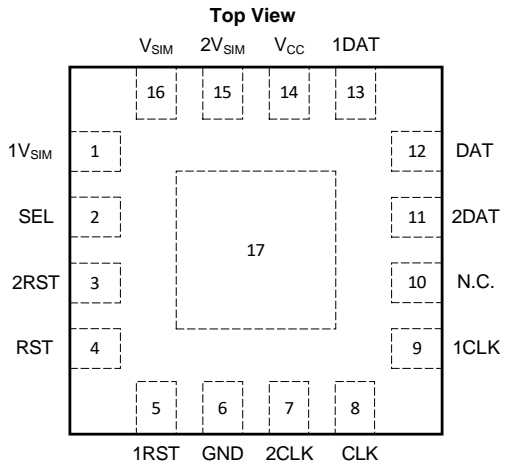


Fig.5-1. GT4899B: QE (QFN3x3-16L) Package

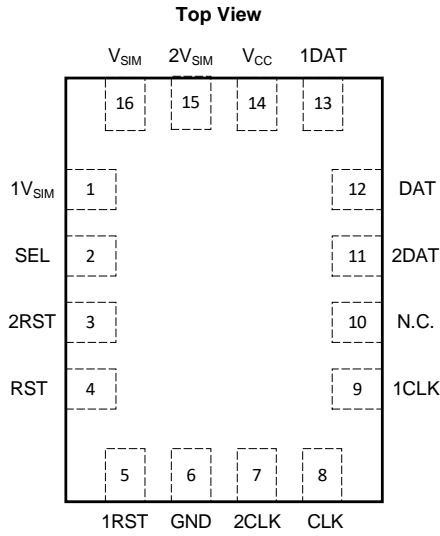


Fig.5-2. GT4899B: UE (UTQFN1.8x2.6-16L) Package

Table 5-2 Pin definition

Pin		Description
Name	QE UE	
1V _{SIM}	1	SIM supply output 1
SEL	2	Select input
2RST	3	RST normally open terminal
RST	4	RST common terminal
1RST	5	RST normal closed terminal
GND	6	Ground
2CLK	7	CLK normally open terminal
CLK	8	CLK common terminal
1CLK	9	CLK normal closed terminal
N.C.	10	Not connect
2DAT	11	DAT normally open terminal
DAT	12	DAT common terminal
1DAT	13	DAT normal closed terminal
V _{CC}	14	Supply Voltage
2V _{SIM}	15	SIM supply output 2
V _{SIM}	16	SIM supply input

带格式的: 下标

带格式的: 下标

带格式的: 下标

Table 5-3 Function Table

SEL	Switch state
0	1DAT=DAT,1RST=RST,1CLK=CLK,1V _{SIM} =V _{SIM}
1	2DAT=DAT,2RST=RST,2CLK=CLK,2V _{SIM} =V _{SIM}

6 Voltage, Temperature, ESD and Thermal Ratings

6.1 Absolute Maximum Ratings⁽¹⁾

Parameters		Min.	Max.	Unit
V _{CC}	Supply voltage range	-0.3	6.0	V
V _{IN}	Input voltage range	-0.3	6.0	V
	Analog, Digital voltage Range ⁽²⁾	-0.3	V _{CC} +0.3	V
	Continuous Current NO,NC or COM	-100	+100	mA
I _{PEAK}	Continuous Channel current	-160	+160	mA
T _J	Junction temperature	-65	150	°C
T _{stg}	Storage temperature	-65	150	°C

(1) Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

6.2 ESD Ratings

ESD			Value	Unit
V(ESD)	Electrostatic discharge	Human-Body Model (HBM) ⁽¹⁾	3.5 K	V
		Charged-Device Model (CDM) ⁽²⁾	2 K	V

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

6.3 Recommended Operating Conditions⁽¹⁾

Over operating free-air temperature range (unless otherwise noted)

Symbol	Parameter		Min	Max	Units
V _{CC}	Supply voltage	Operating	1.65	5.5	V
V _{CTRL}	Control Input voltage	Operating	0	V _{CC}	V
V _{SW}	Switch I/O voltage	Operating	0	V _{CC}	V
T _A	Ambient temperature		-40	85	°C

(1) All unused digital inputs of the device must be held at V_{CC} or GND to ensure proper device operation.

7 Electrical Specifications

7.1 DC Electrical Characteristics

V_{CC} =1.65V to 5.5V, FULL=-40°C to +125°C. Typical values are at T_A =+25°C (unless otherwise noted)

Parameter	Symbol	Conditions	V_{CC}	Temp	Min	Typ	Max	Units
Analog Signal Range	V_{NO}, V_{NC}, V_{COM}				0		V_{CC}	V
On-Resistance	R_{ON}	V_{NO} or $V_{NC}=V_{CC}/2$, $I_{COM}=-100mA$	4.5V	+25°C		3.5	6	Ω
			3V	+25°C		7	10	
			2.3V	+25°C		12	22	
			1.65V	+25°C		15	27	
On-Resistance Match Between Channels	ΔR_{ON}	V_{NO} or $V_{NC}=V_{CC}/2$, $I_{COM}=-100mA$	4.5V	+25°C		0.3	1	Ω
			3V	+25°C		1	3	
On-Resistance Flatness	RFLAT (ON)	$0 \leq V_{NO}$ or $V_{NC} \leq V_{CC}$, $I_{COM}=-100mA$	4.5V	+25°C		1	1.5	Ω
			3V	+25°C		3.5	5.5	
			2.3V	+25°C		9	14	
			1.65V	+25°C		12	18	
NC,NO Off Leakage Current	$I_{NC(OFF)}, I_{NO(OFF)}$	V_{NO} or $V_{NC}=1V, 4.5V, V_{COM}=4.5V, 1V$	5.5 V	Full			1	μA
NC,NO,COM On Leakage Current	$I_{NC(ON)}, I_{NO(ON)}, I_{COM(ON)}$	$V_{COM}=1V, 4.5V, V_{NO}$ or $V_{NC}=1V, 4.5V$, or floating	5.5 V	Full			1	μA
Input High Voltage	V_{INH}		4.5V	Full	1.8			V
			3V	Full	1.5			V
Input Low Voltage	V_{INL}		4.5V	Full			0.5	V
			3V	Full			0.4	V
Input Leakage Current	I_{IN}	$V_S=V_{CC}$ or 0	5.5 V	Full			1	μA
Power Supply Current	I_{CC}	$V_S=GND$ or V_{CC}	5.5 V	Full			1	μA

7 Electrical Specifications(Continued)

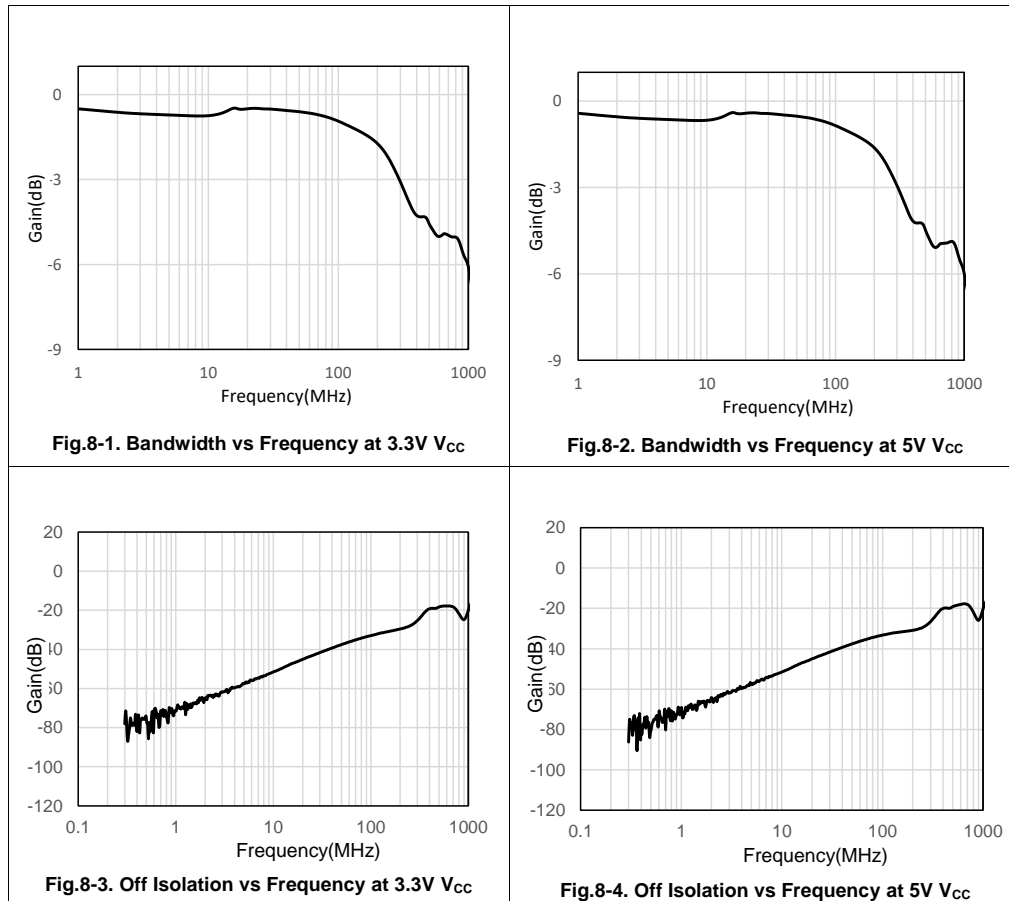
7.2 Switch And AC Characteristics

V_{CC}=1.65V to 5.5V, FULL=-40°C to +125°C. Typical values are at TA=+25°C (unless otherwise noted)

Parameter	Symbol	Conditions	V _{CC}	Temp	Min	Typ	Max	Units
Turn-On Time	t _{ON}	V _{COM} =V _{CC} , R _L =300Ω, C _L =35pF	5V	+25°C		10		ns
			3.3V			14		
Turn-Off Time	t _{OFF}	V _{COM} =V _{CC} , R _L =300Ω, C _L =35pF	5V	+25°C		20		ns
			3.3V			22		
Break-Before-Make Time Delay	t _{BBM}	V _{NO1} =V _{NC1} =V _{NO2} =V _{NC2} =3V R _L =300Ω, C _L =35pF	5V	+25°C		12		ns
			3.3V			20		
Off Isolation	O _{ISO}	R _L =50Ω, Switch OFF	3.3V	+25°C		-52		dB
					f=1MHz		-73	
Crosstalk Isolation	X _{TALK}	R _L =50Ω, Switch OFF, f=10MHz	3.3V	+25°C		-89		dB
-3dB Bandwidth	BW	R _L =50Ω, Switch ON	3.3V	+25°C		350		MHz
Charge Injection	Q	C _L =1nF, V _{GEN} =0V, R _{GEN} =0Ω	5V	+25°C		6		pC
			3.3V			4		
NC,NO Off Capacitance	C _{NC(OFF)} , C _{NO(OFF)}	f=1MHz	3.3V	+25°C		15		pF
NC,NO.COM On Capacitance	C _{NC(ON)} , C _{NO(ON)} , C _{COM(ON)}	f=1MHz	3.3V	+25°C		8		pF

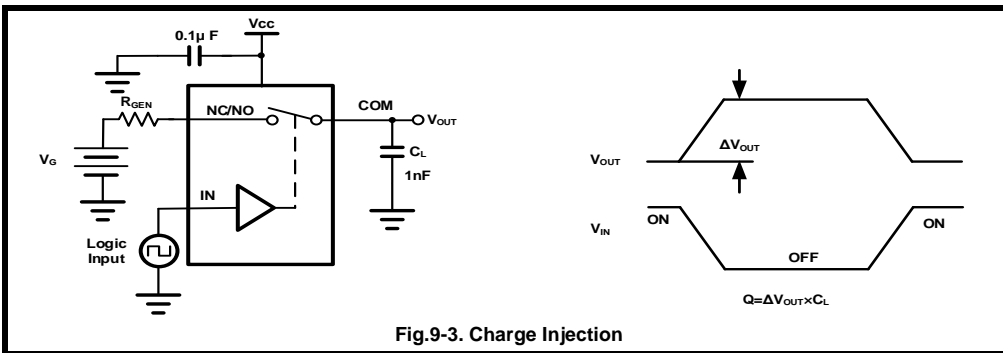
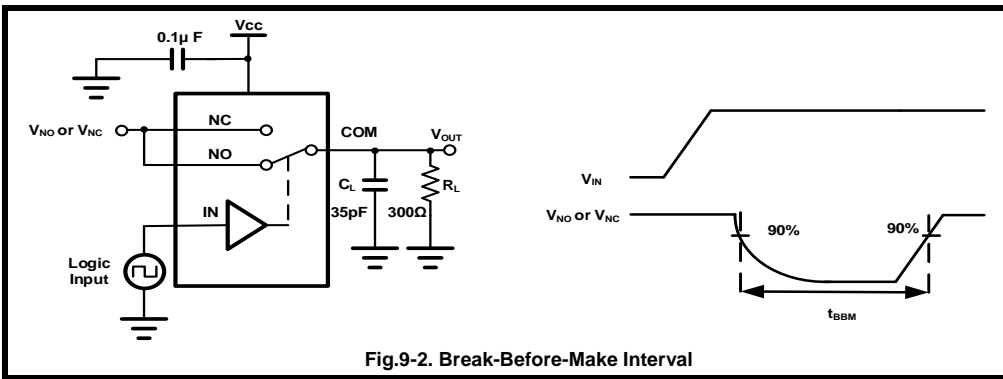
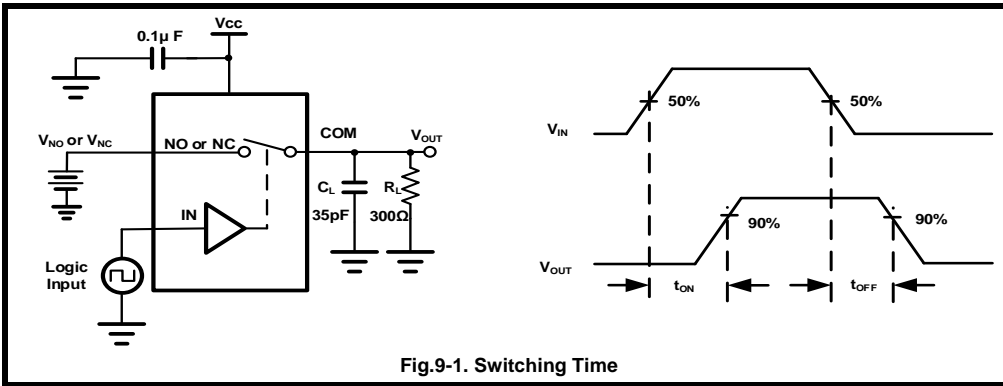
8 Typical Characteristics

V_{CC}=1.65V or 5.5V, FULL=-40°C to +125°C. Typical values are at TA=+25°C (unless otherwise noted)



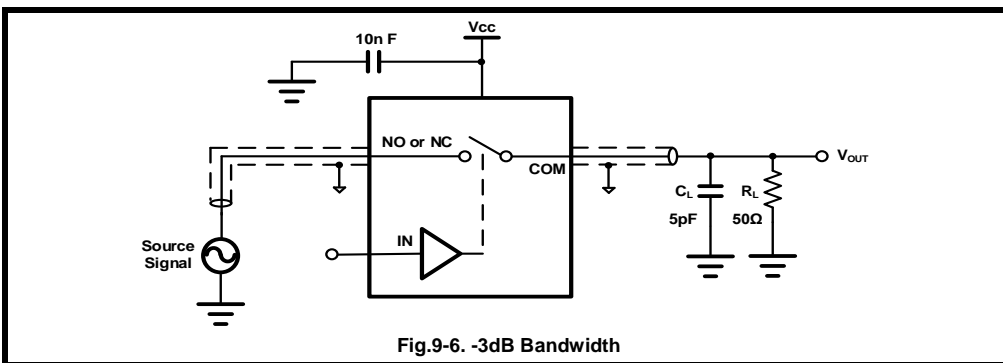
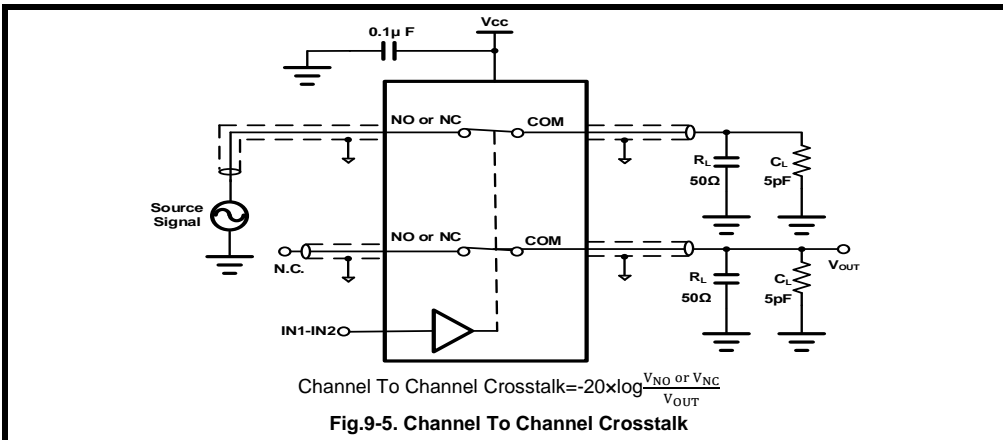
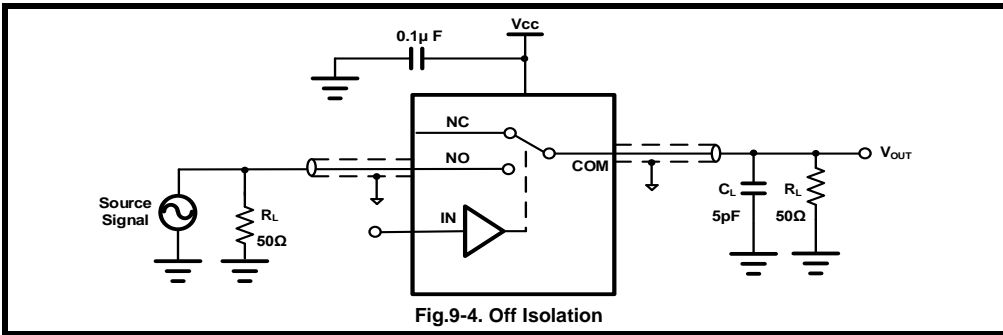
9 Measurement Information

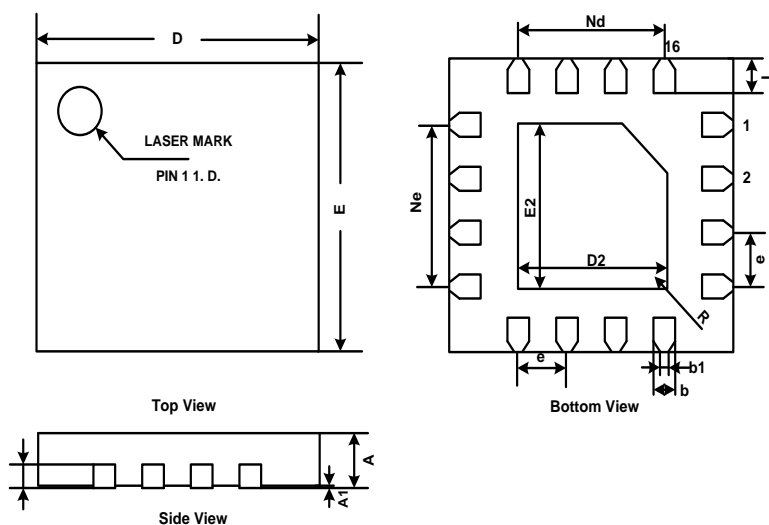
COM=V_{SIM}/RST/CLK/DAT, NC=1V_{SIM}/1RST/1CLK/1DAT, NO=2V_{SIM}/2RST/2CLK/2DAT



9 Measurement Information(Continued)

COM= $V_{SIM}/RST/CLK/DAT$, NC= $1V_{SIM}/1RST/1CLK/1DAT$, NO= $2V_{SIM}/2RST/2CLK/2DAT$

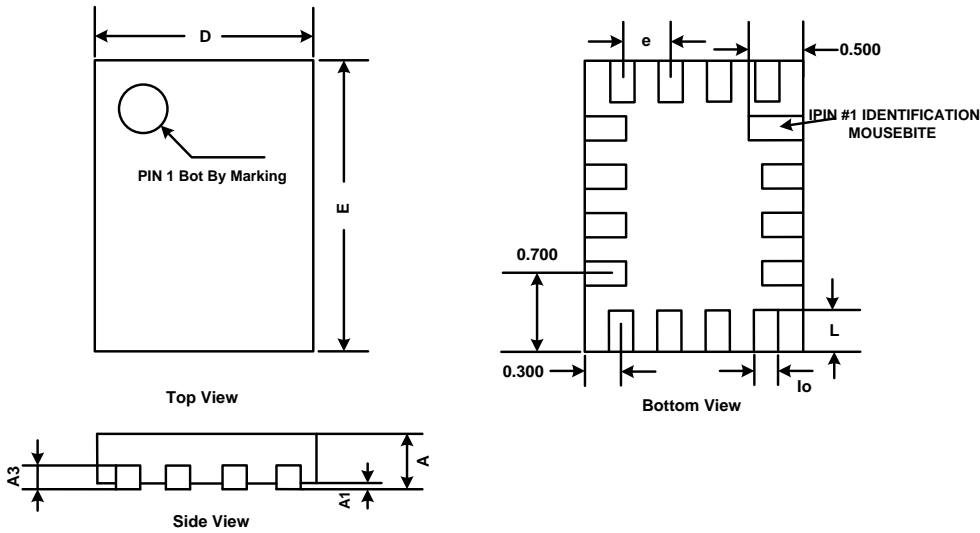


10 Package Outline Dimension
QFN3x3-16L


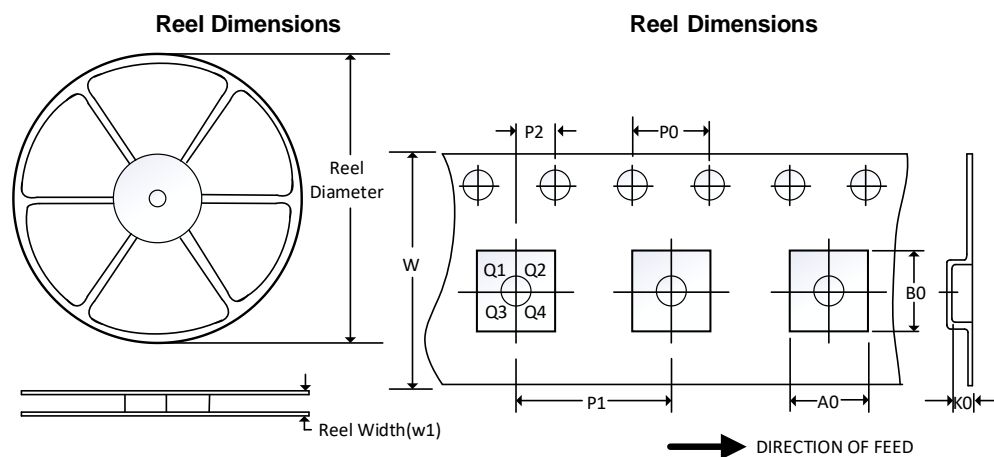
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Min
A	0.700	0.800	0.028	0.032
A1	0.000	0.050	0.000	0.002
b	0.200	0.300	0.008	0.012
b1	0.230REF		0.009REF	
c	0.203REF		0.008REF	
D	2.900	3.100	0.116	0.124
D2	1.600	1.700	0.064	0.068
e	0.500BSC		0.020BSC	
Nd	1.500BSC		0.060BSC	
Ne	1.500BSC		0.060BSC	
E	2.900	3.100	0.116	0.124
E2	1.600	1.700	0.064	0.068
L	0.350	0.450	0.014	0.018
h	0.250	0.350	0.010	0.014
K	0.225	0.325	0.009	0.013
R	0.075REF		0.003REF	

10 Package Outline Dimension(Continued)

UTQFN1.8x2.6-16L



Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min	Nom	Max	Min	Nom	Max
A	>0.50	0.55	0.60	>0.19	0.21	0.23
A1	0.00	-	0.05	0.00	-	0.02
A3	0.15REF			0.06REF		
D	1.75	1.80	1.85	0.68	0.70	0.72
E	2.55	2.60	2.65	0.99	1.01	1.03
L	0.30	0.40	0.50	0.12	0.16	0.19
lo	0.15	0.20	0.25	0.06	0.08	0.10
e	0.40BSC			0.16BSC		

12 Tape and Reel Information


NOTE: The picture is only for reference. Please make the object as the standard.

Key Parameter List of Tape and Reel

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
QFN3x3-16L	178mm	12	3.23	3.23	1.05	4	4	2	12	Q1
UTQFN1.8x2.6-16L	178mm	9.04	2.1	2.9	0.75	4	4	2	8	Q1

NOTE:
 1. All dimensions are nominal.
 2. Plastic or metal protrusions of 0.15mm maximum per side are not included.